



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **NAKAJYO, SHINSUKE et al**

Group Art Unit: **2824**

Serial No.: **09/686,958**

Examiner: **PYONIN, ADAM**

Filed: **October 12, 2000**

P.T.O. Confirmation No.: 5708

FOR: **MANUFACTURE OF WAFER LEVEL SEMICONDUCTOR  
DEVICE AND SEMICONDUCTOR DEVICE**

**RESPONSE TO THE RESTRICTION REQUIREMENT**  
**DATED APRIL 22, 2002**

Commissioner for Patents  
Washington, D.C. 20231

Date: May 10, 2002

Sir:

This paper is submitted in response to the Official Action dated **April 22, 2002**.

In the Action, restriction is required between Group (I), Claims 1-5, drawn to method;  
and Group (II), Claims 6-7, drawn to device.

Applicants hereby elect the subject matter of Group (I), Claims 1-5 for prosecution in this  
application. This election is made without traverse, it being understood that the applicants' rights  
to the filing of a divisional application directed to the non-elected subject matter under 35 USC  
120 and 35 USC 121 are retained.

In the event that this paper is not timely filed, applicants hereby petition for an  
appropriate extension of time. The fee for any such extension may be charged to our Deposit  
Account No. 01-2340.

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In the event any additional fees are required in connection with this response, please charge our Deposit Account No. 01-2340.

Respectfully Submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP



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